

## Q. P. Code: 23921

## 3 Hours

Total Marks: 80

## Instructions:

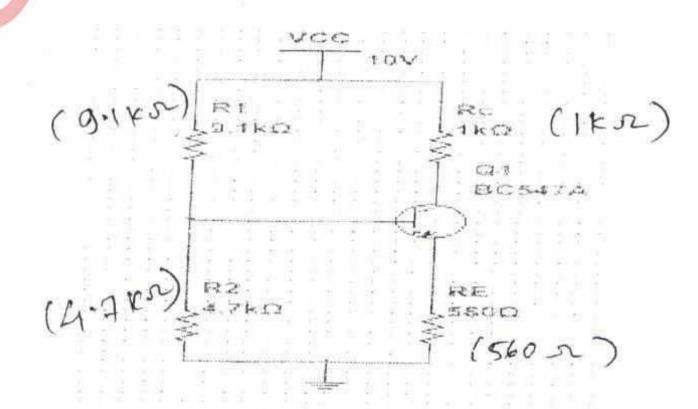
- Question Number 1 is Compulsory
- Attempt Any Three from the remaining 5 Questions
- Figures to right indicate the full marks
- Assume the suitable data if necessary.



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- Draw and explain the construction of Junction Field Effect Transistor
- Explain the VI characteristic of Schottky Diode and give the b applications of it.
- Explain in brief the h-parameter model in BJT
- What is Darlington configuration? Why is it used in BJT? d
- A Hartley Oscillator circuit having two individual inductors of 0.5mH each are designed to resonate in parallel with a variable capacitor used is of 100pF. Determine the frequency of oscillation
- State the advantages of negative feedback f
- State the various types of negative feedback amplifiers and compare 10 Que. 2 these amplifiers in terms of block diagram, feedback gain, bandwidth, Noise, frequency distortion, stability and amplifier type
  - Explain the features of multistage RC coupled amplifier. Draw the b neat circuit diagram of two stage R-C coupled transistor amplifier and also give its frequency response
- State the Barkhausen's criteria used in oscillators and explain the R-C 10 Que. 3 phase shift oscillator in detail
  - Give the comparison between C, LC and CLC and CLC filters that are 10 used in rectifiers.
- State the various biasing techniques used in BJT and calculate the 10 Que. 4 values of IBQ, ICQ and VCEQ for the following circuit shown of Silicon transistor with  $\beta=100$



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	b	Draw and explain D-type MOSFET in detail with its input and output characteristics.
Que. 5	a	Give the complete AC analysis of CE amplifier using either h parameter model or re model.
	b	Differentiate JFET and MOSFET in terms of symbol, construction, Input impedance, biasing methods used and also source and drain characteristics.
Que 6	Write	e a short note on any two of the following
	a	UJT relaxation oscillator
	b	Thermal stabilization and compensation
	С	Concept of DC load Line used in BJT
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