Q. P. Code: 21318

(Time: 3Hrs)   Max Marks: 80

NB: 1. Question No.1 Compulsory.
    2. Solve any THREE from Q.2 to Q.6
    3. Assume suitable data whenever necessary with justification.

Q1.   Solve any FOUR.

    (A)   Explain Memory banks for 8086 Processor   (5)

    (B)   Draw and Explain Floating Point Pipeline for Pentium Processor.   (5)

    (C)   Explain Multitasking and Protection for 80386 processor   (5)

    (D)   Explain Flag Register bits of 8086.   (5)

    (E)   Explain Virtual Mode (VM86) 80386 Processor.   (5)

Q2. (A)   Explain Interrupt Structure of 8086 Processor.   (10)

     (B)   Explain PPI 8255 with block diagram.   (10)

Q3. (A)   Draw and Explain write operation timing diagram for maximum mode.   (10)

     (B)   Explain Operating Modes of PIC 8259.   (10)

Q4. (A)   Explain following instructions.   (10)
           DAA, AAA, XLAT, LAHF

     (B)   Explain Segment Descriptor of 80386 Processor.   (10)

Q5. (A)   Explain Gate type of descriptors.   (10)

     (B)   Explain Data Cache architecture for Pentium Processor.   (10)

Q6. (A)   Explain SPARC Processor with block diagram.   (10)

     (B)   Explain with block diagram PIT 8254   (10)

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